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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Robert M. Zeidman

Serial No.: 10/688,573

Filed: October 20, 2003

For: Software Tool for Synthesizing a
Real-Time Operating System

Examiner: Ben C. Wang

Art Unit: 2196

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Transmittal for an Amendment and Response to Final Office Action

Dear Sir:

In response to the Final Office Action mailed March 13, 2007, please find enclosed:

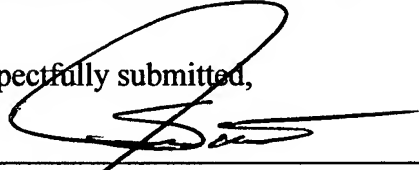
- (1) An Amendment and Response to Final Office Action; and
- (2) Postcard.



Please associate this patent application with Customer No. **48809**.

Date: May 3, 2007

Respectfully submitted,



Jim H. Salter

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FIRST CLASS MAIL CERTIFICATE OF MAILING

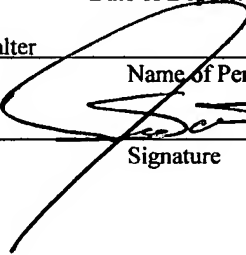
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313-1450

on May 3, 2007

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Jim H. Salter

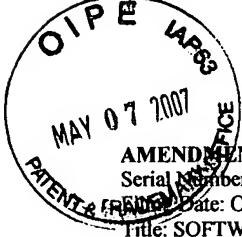
Name of Person Mailing Correspondence



Signature

May 3, 2007

Date



AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Serial Number: 10/688,573

Filed Date: October 20, 2003

Title: SOFTWARE TOOL FOR SYNTHESIZING A REAL-TIME OPERATING SYSTEM

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Dkt: Zeid-01

S/N 10/688,573

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Robert M. Zeidman	Examiner:	Ben C. Wang
Serial No.:	10/688,573	Group Art Unit:	2196
Filed:	October 20, 2003	Docket No.:	Zeid-01
Title:	SOFTWARE TOOL FOR SYNTHESIZING A REAL-TIME OPERATING SYSTEM		

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116

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P.O. Box 1450
Alexandria, VA 22313-1450

This responds to the Final Office Action mailed on March 13, 2007. Please amend the above-identified patent application as follows to put the case in condition for allowance.

IN THE CLAIMS

Please amend the claims as follows:

- 1) (Currently Amended) A method for developing a real-time operating system, comprising:
 - a) specifying a set of n tasks, task(1) through task(n), to be scheduled for execution, ~~at least one of the tasks of said set of n tasks being selectively configurable as a preemptive or a non-preemptive task;~~
 - b) specifying a scheduling algorithm for scheduling the execution of said set of n tasks; and
 - c) synthesizing source code ~~with embedded from~~ commands embedded in source code to implement a task scheduler that uses said scheduling algorithm ~~and said embedded commands~~ for controlling execution of said set of n tasks, said synthesized source code being executable on a target system after compilation.
- 2) (Currently Amended) The method of claim 1) ~~further~~ including specifying t init-tasks that are executed only once upon initial execution of said task scheduler, t being less than or equal to n.
- 3) (Currently Amended) The method of claim 1) ~~further~~ including specifying f f-loop tasks, each having an associated integer value lc(i) for i ranging from 1 to f and f being less than or equal to n, said task scheduler including a continuously executing loop such that each f-loop task executes exactly once every lc(i) times that the loop is executed.